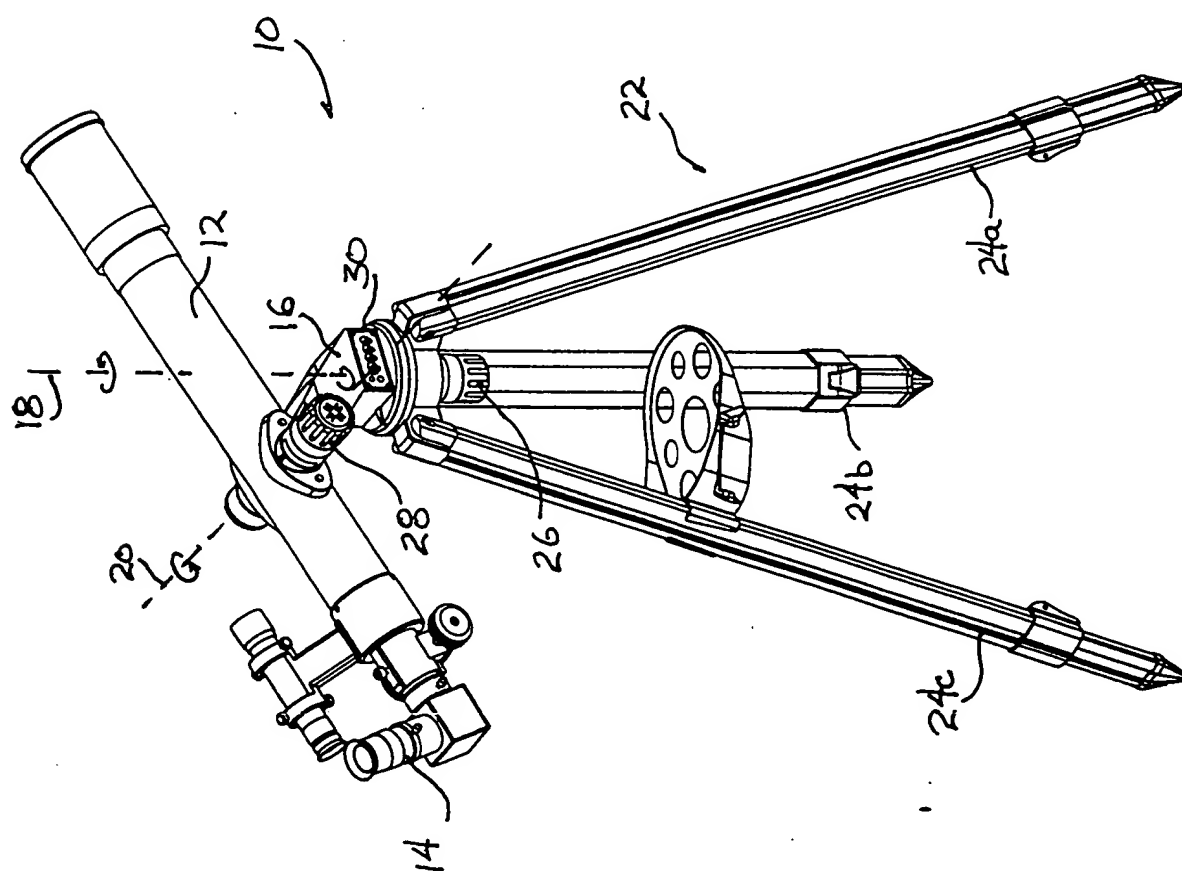


FIG. 1



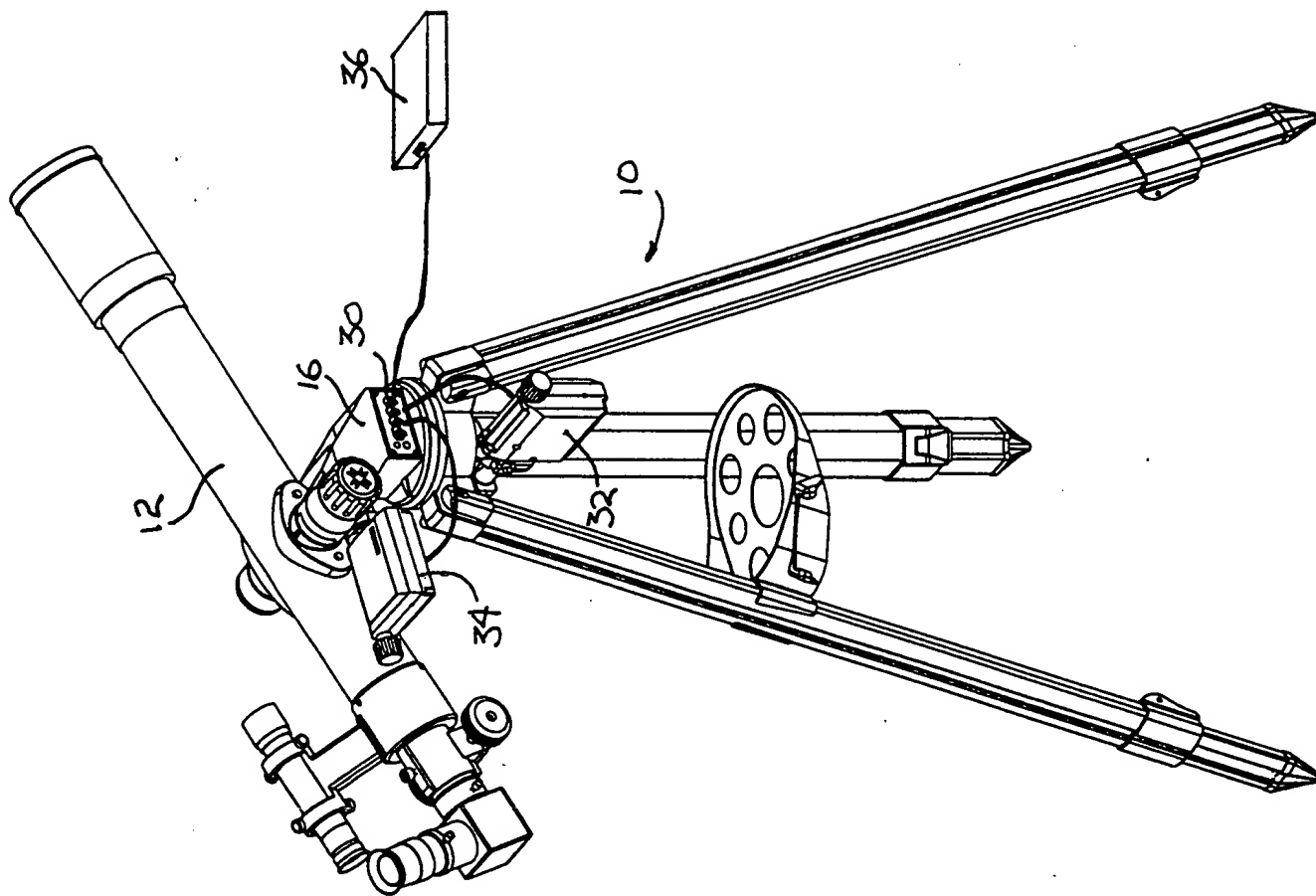


FIG. 2

FIG. 2 is a perspective view of the device of FIG. 1, showing the device in a closed position. The device is shown in a perspective view, with the arms 10, 12, 32, and 34, and the components 16, 30, 34, and 36, all visible. The device is shown in a perspective view, with the arms 10, 12, 32, and 34, and the components 16, 30, 34, and 36, all visible.

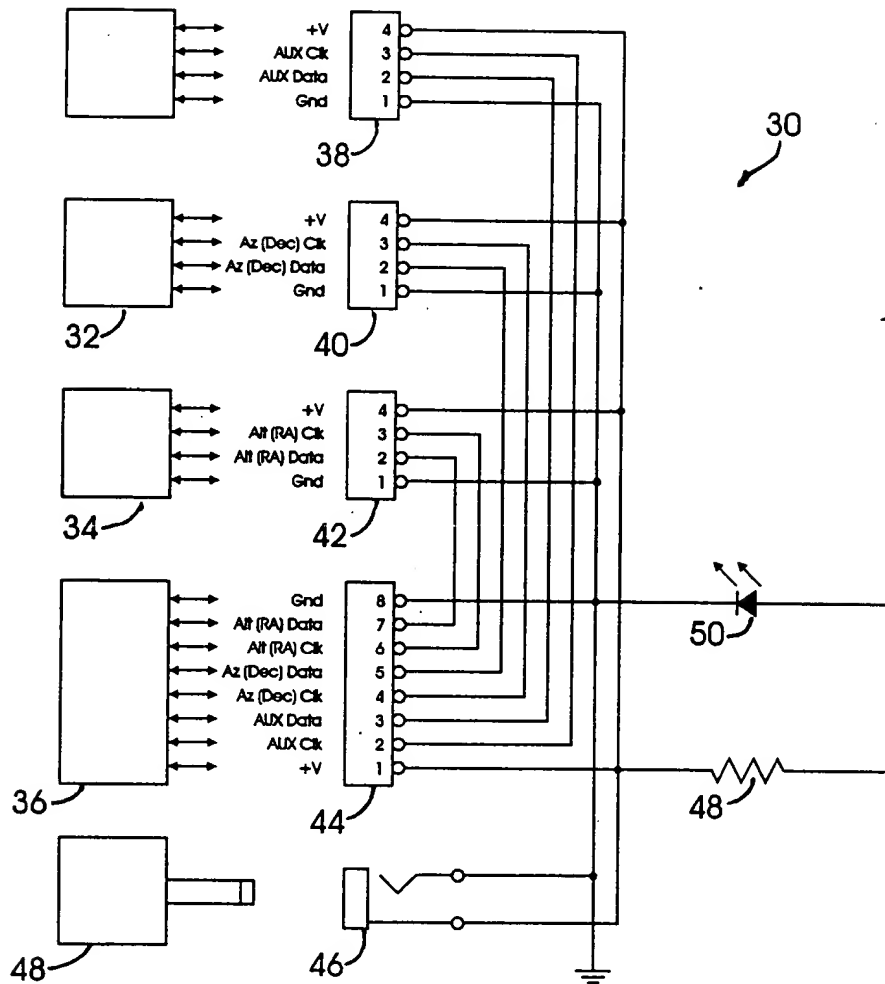


FIG. 3a

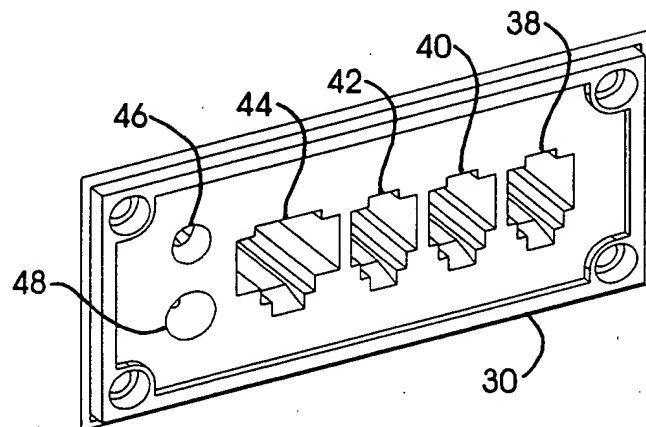


FIG. 3b

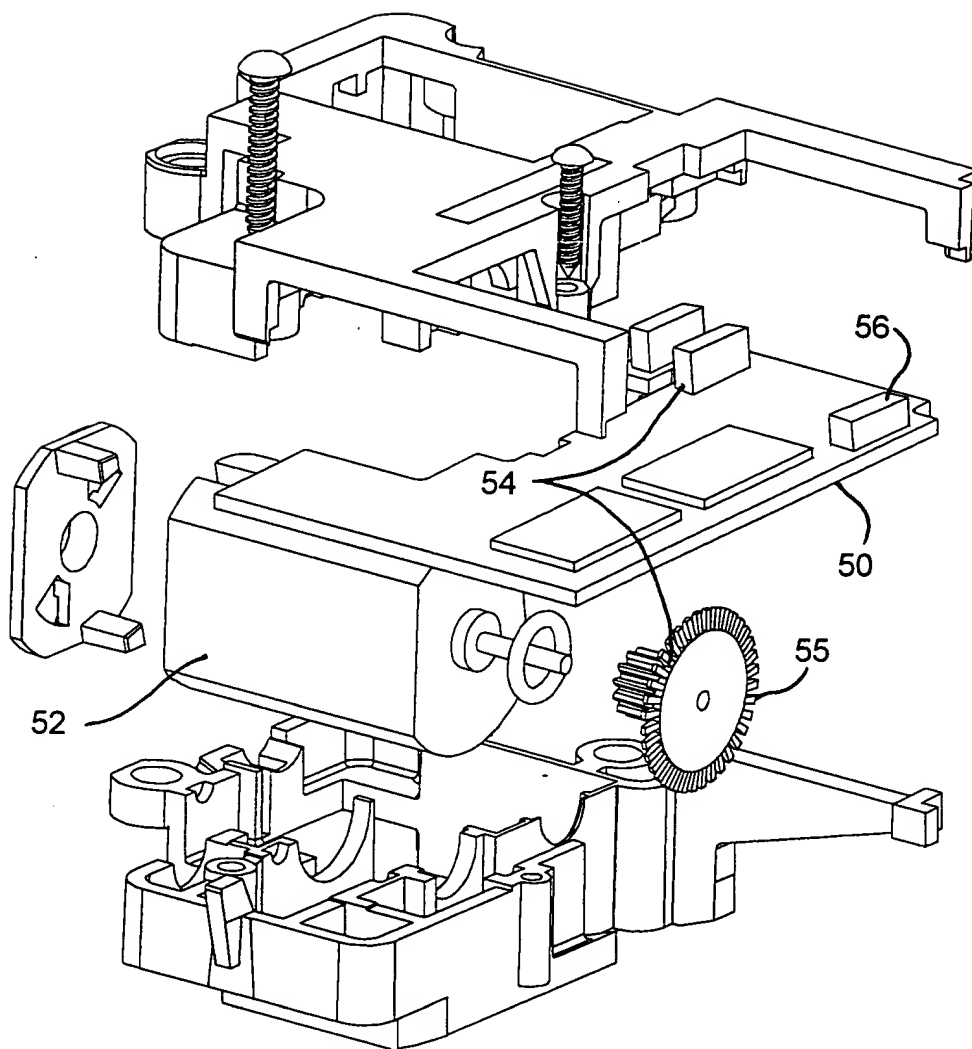


FIG. 4a

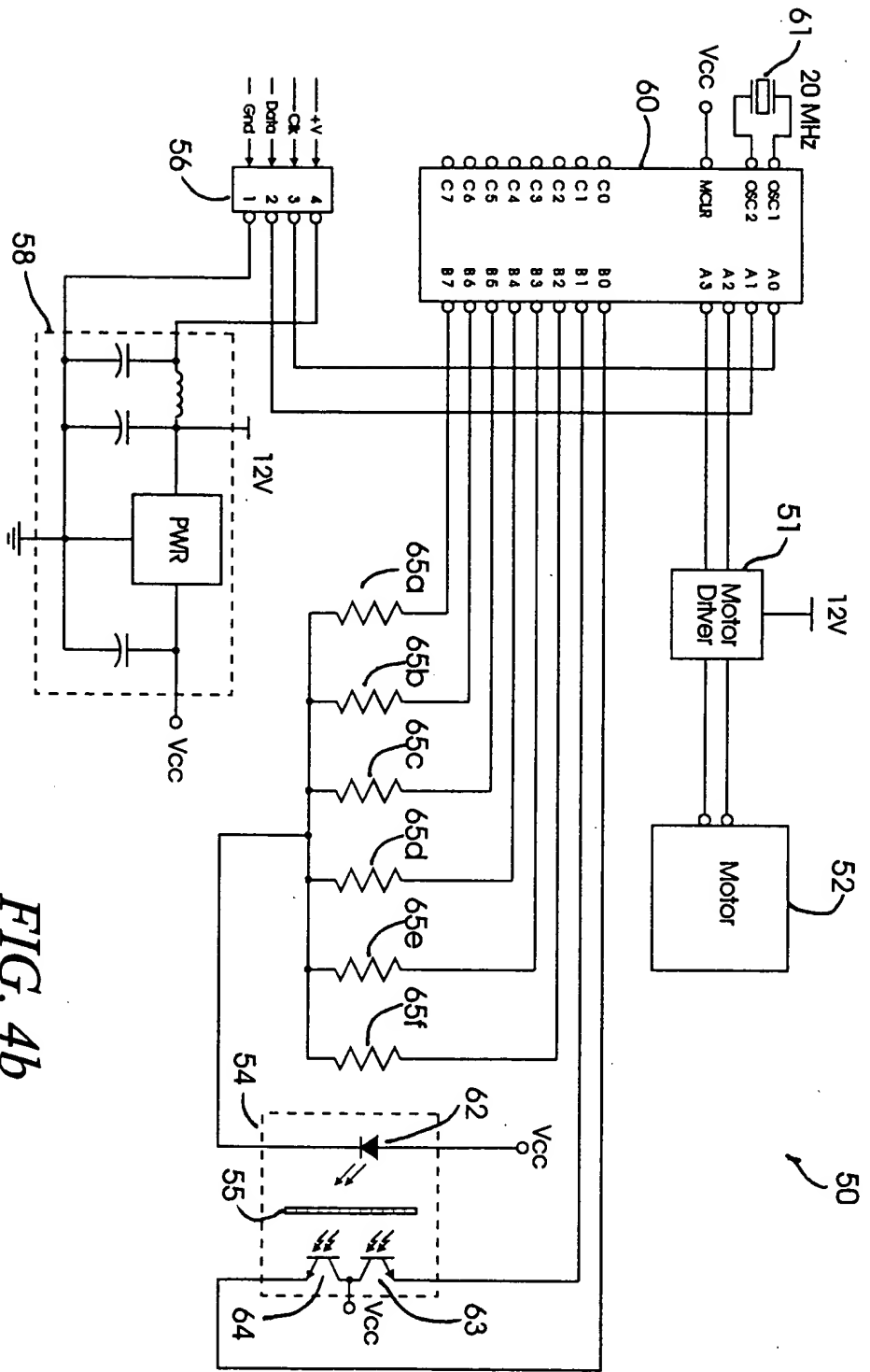


FIG. 4b

FIG. 4b is a block diagram of a motor control system 50. The system includes a microcontroller 60, a motor driver 51, a motor 52, and a power management section 58. The microcontroller 60 is connected to the motor driver 51 and the motor 52. The power management section 58 is connected to the microcontroller 60 and the motor driver 51. The microcontroller 60 has a 20 MHz oscillator 61, a Vcc pin, a Gnd pin, a +V pin, a CK pin, a Data pin, and address lines A0-A3 and data lines B0-B7. The motor driver 51 is connected to the microcontroller 60 and the motor 52. The power management section 58 includes a PWR block and capacitors connected to the microcontroller 60 and the motor driver 51.

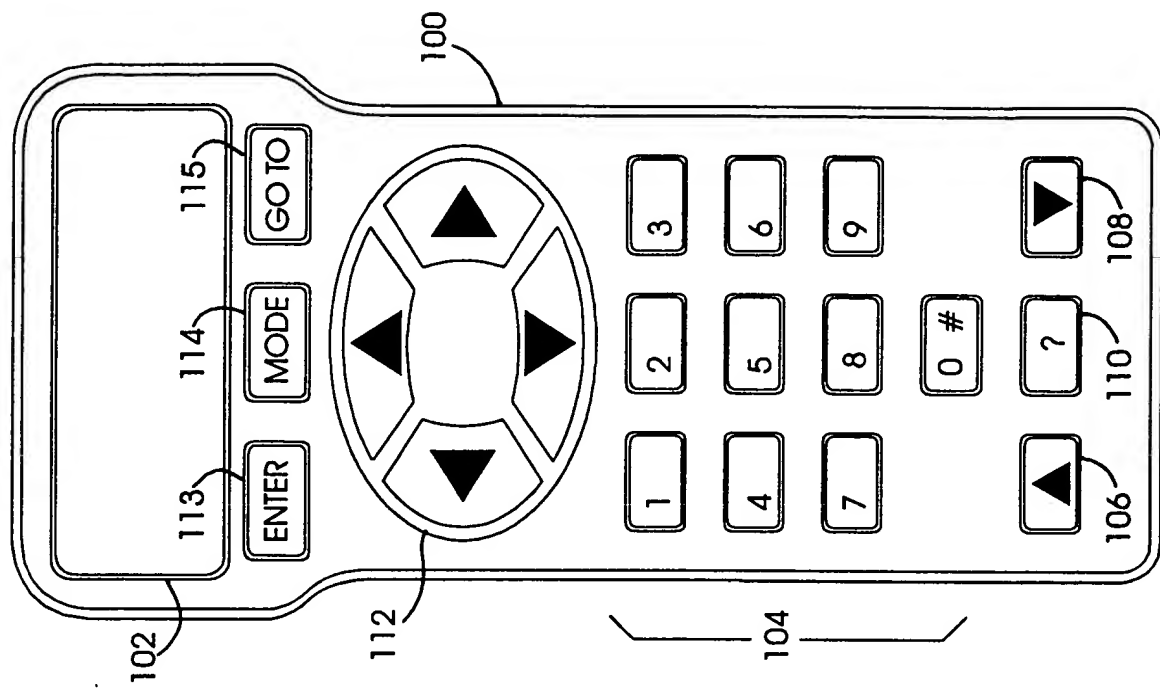


FIG. 6a

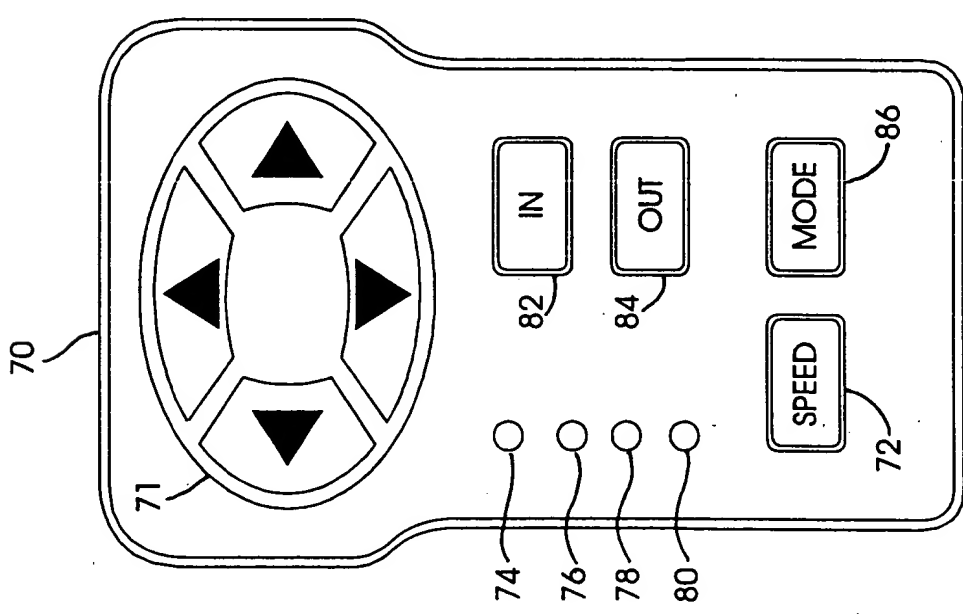


FIG. 5a

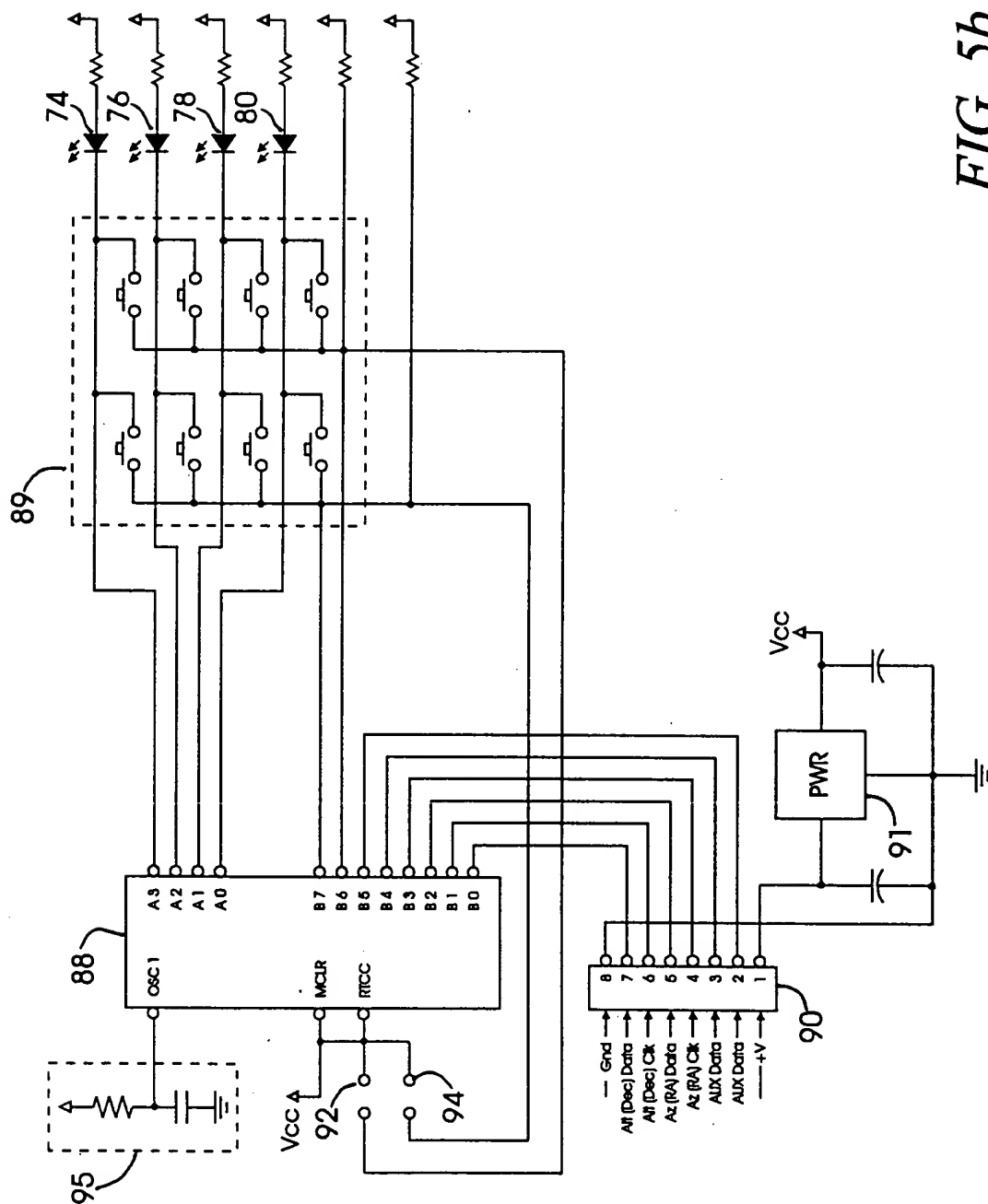


FIG. 5b

FIG. 5b is a block diagram of the microcontroller system 100, showing the microcontroller 88, the oscillator 95, the power supply 91, the input/output 90, the switches 92, 94, and the output drivers 89.

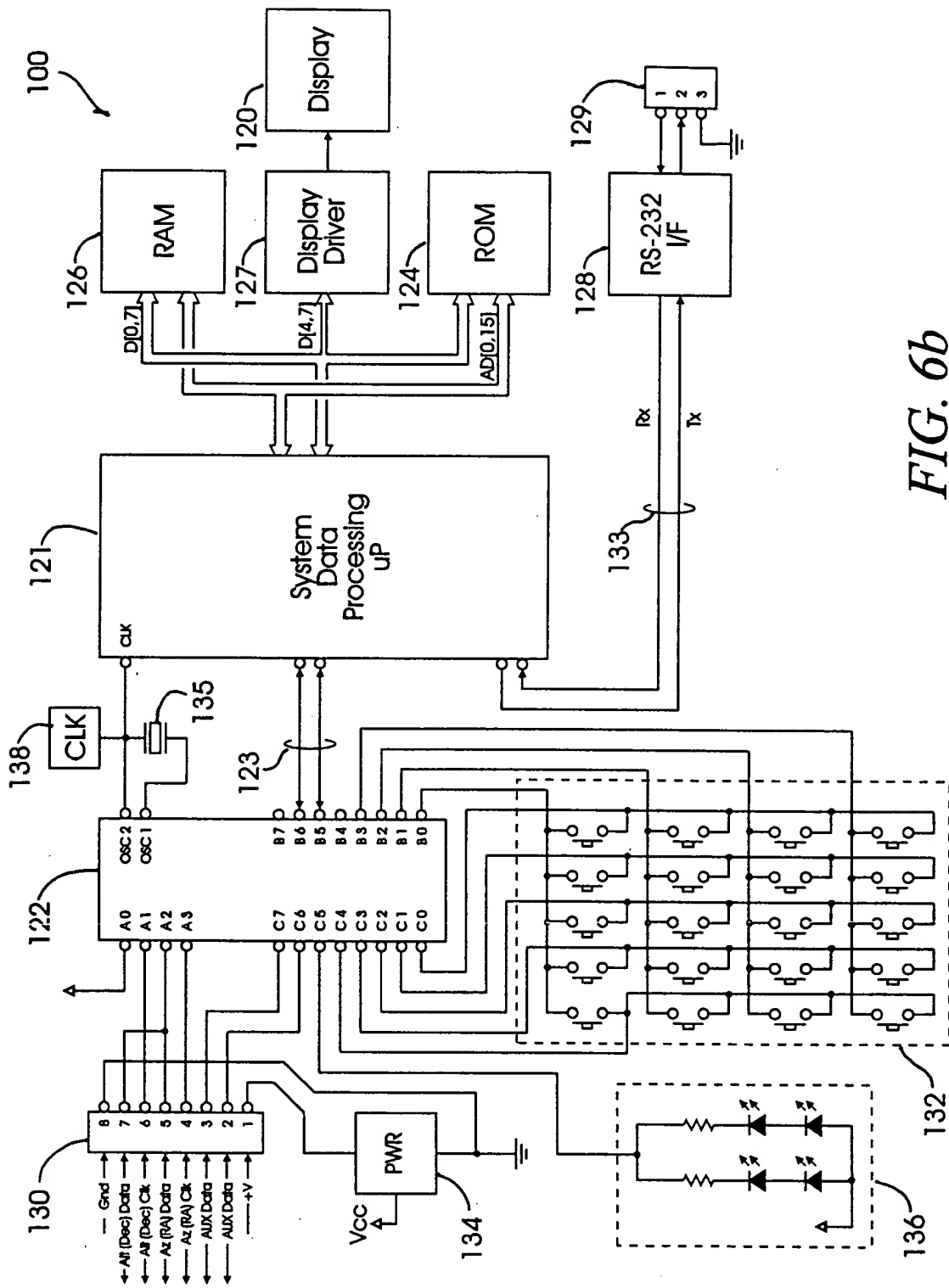


FIG. 6b

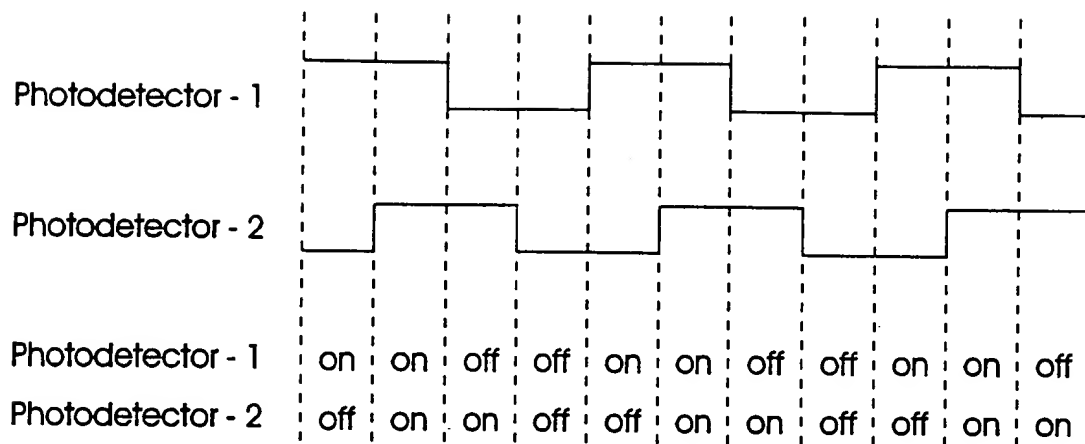


FIG. 7a

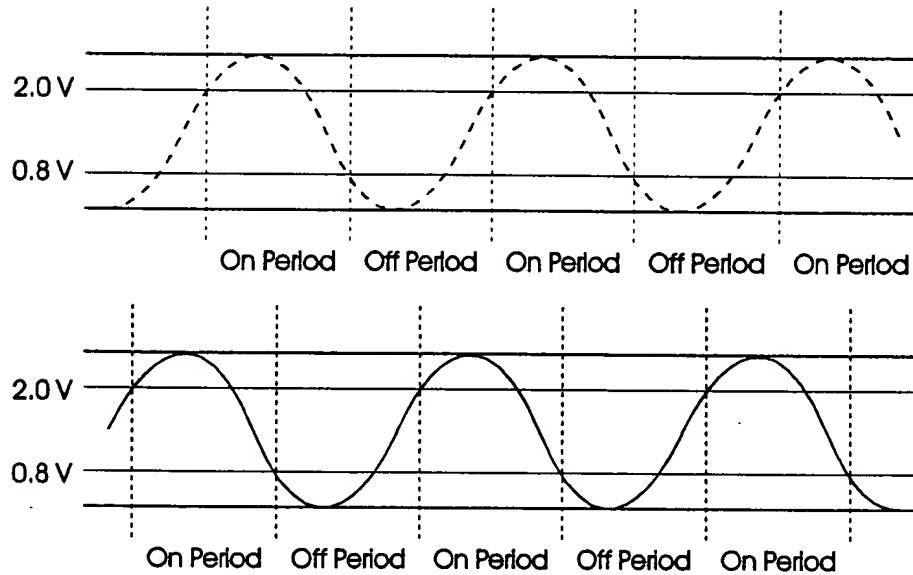


FIG. 7b

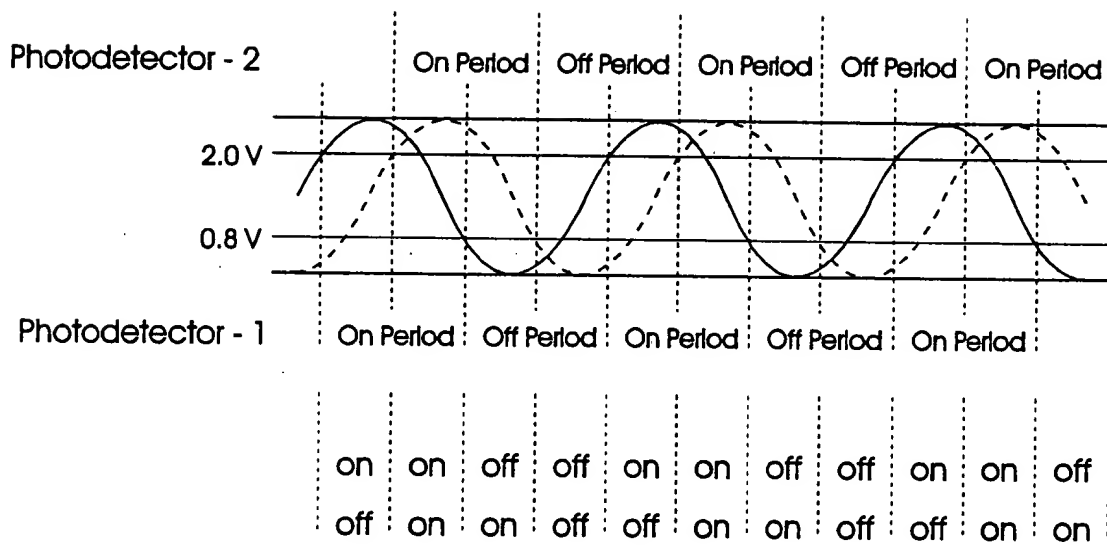


FIG. 7c

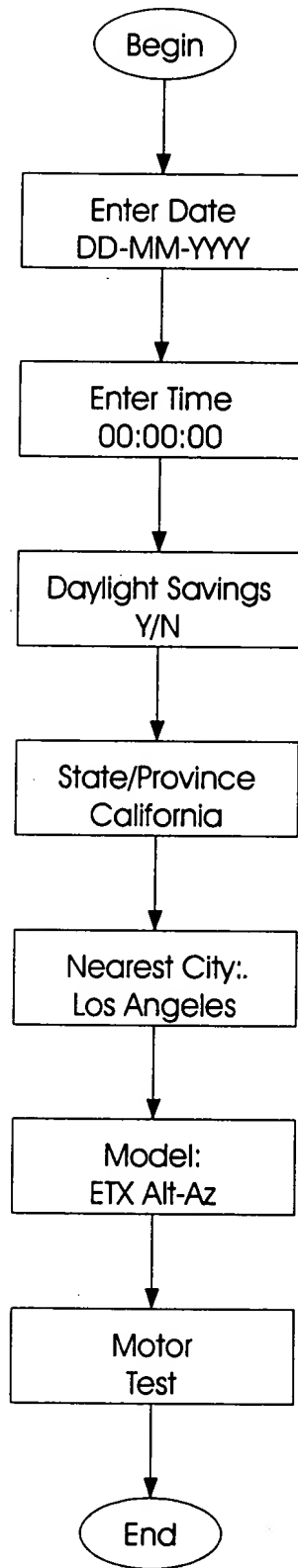


FIG. 8

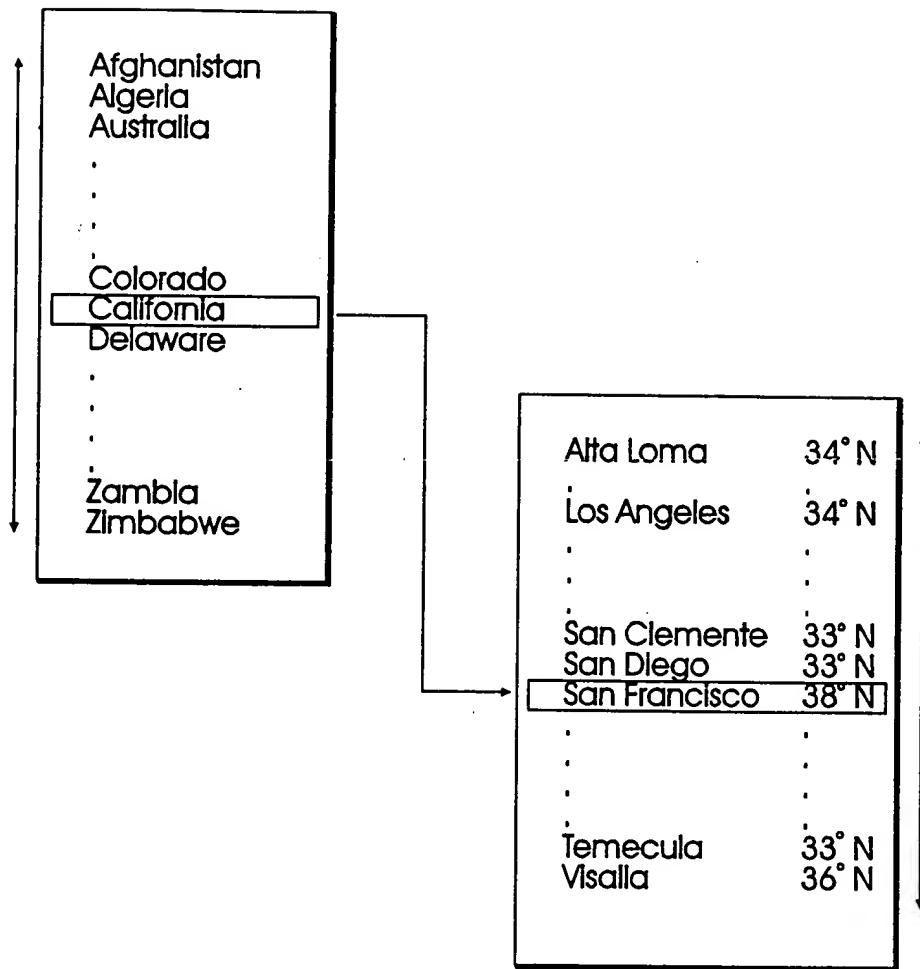


FIG. 9

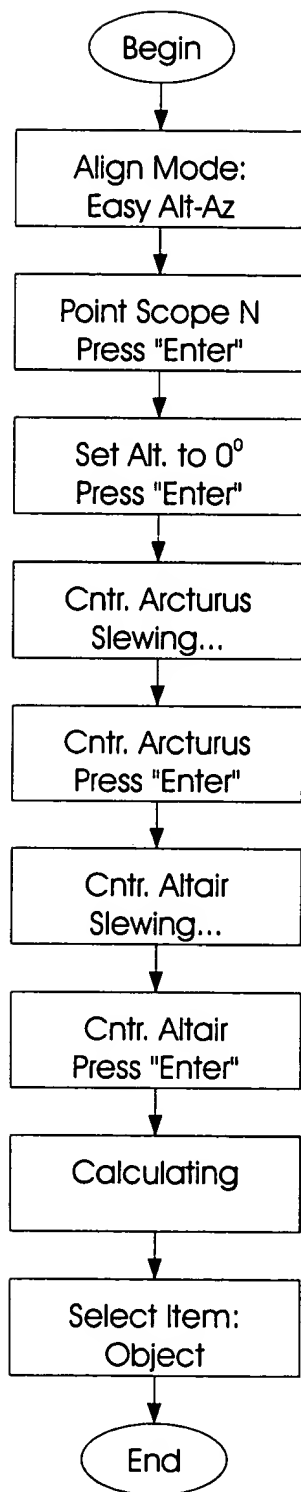


FIG. 10a

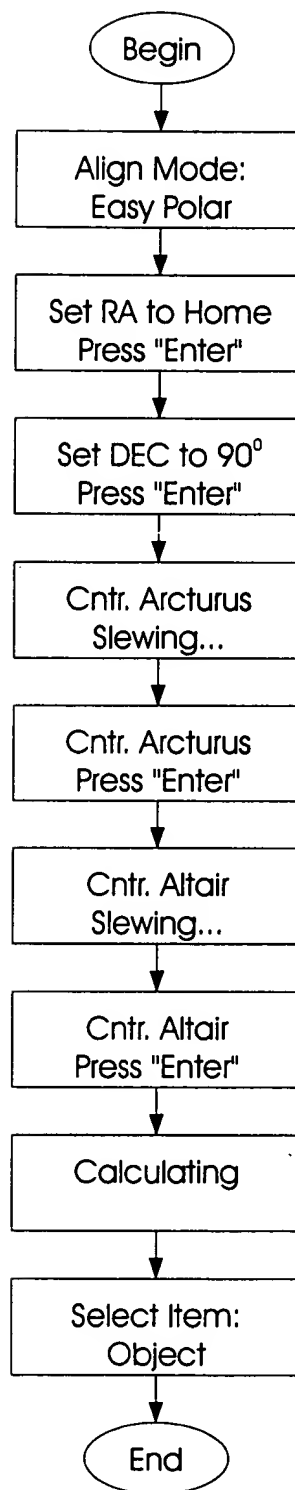


FIG. 11a

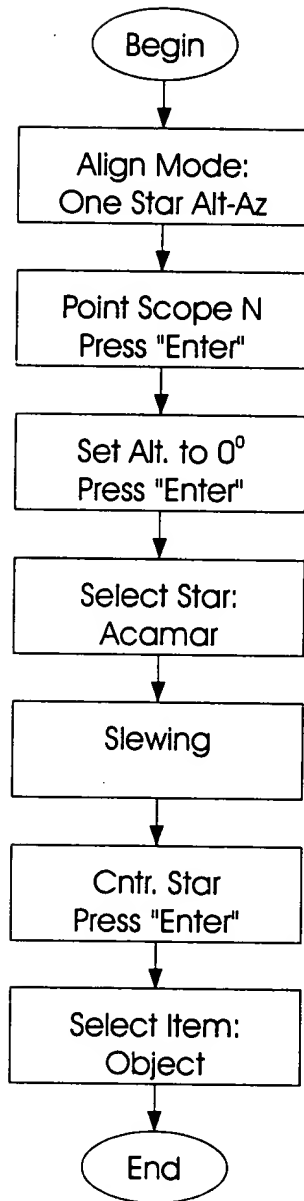


FIG. 10b

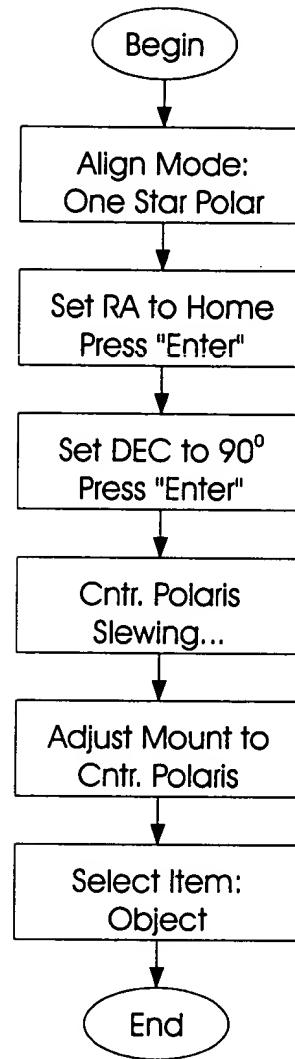


FIG. 11b

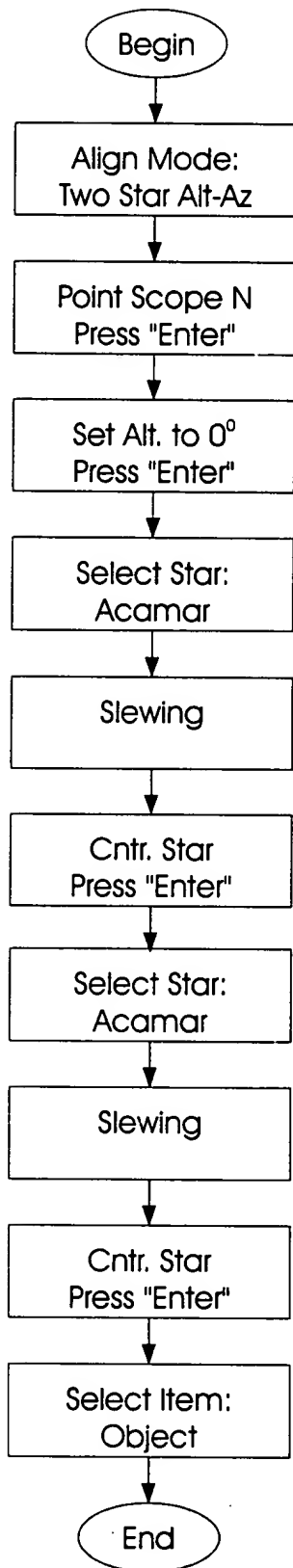


FIG. 10c

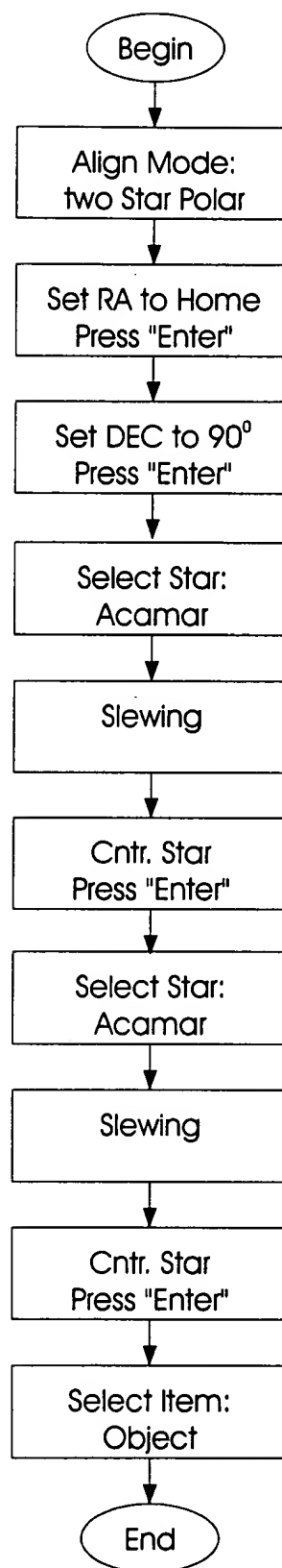


FIG. 11c

Initiating Packet : Master to Target

Byte 0 (>) - n	Count byte, n = 1 to 255
Byte 1 (>) - A	Address select byte, A = 0 - 255: A = 0 = Broadcast
Byte 2 (>) - xx	Command byte, xx = content
•	Intermediate bytes, device unique
•	
•	
•	
Byte n (>) - xx	"Last" byte, device unique

Response Packet : Target to Master

Byte 0 (<) - n	Count byte, n = 1 to 254
•	Intermediate bytes, device unique
•	
•	
•	
Byte n (<) - xx	"Last" byte, device unique

FIG. 12

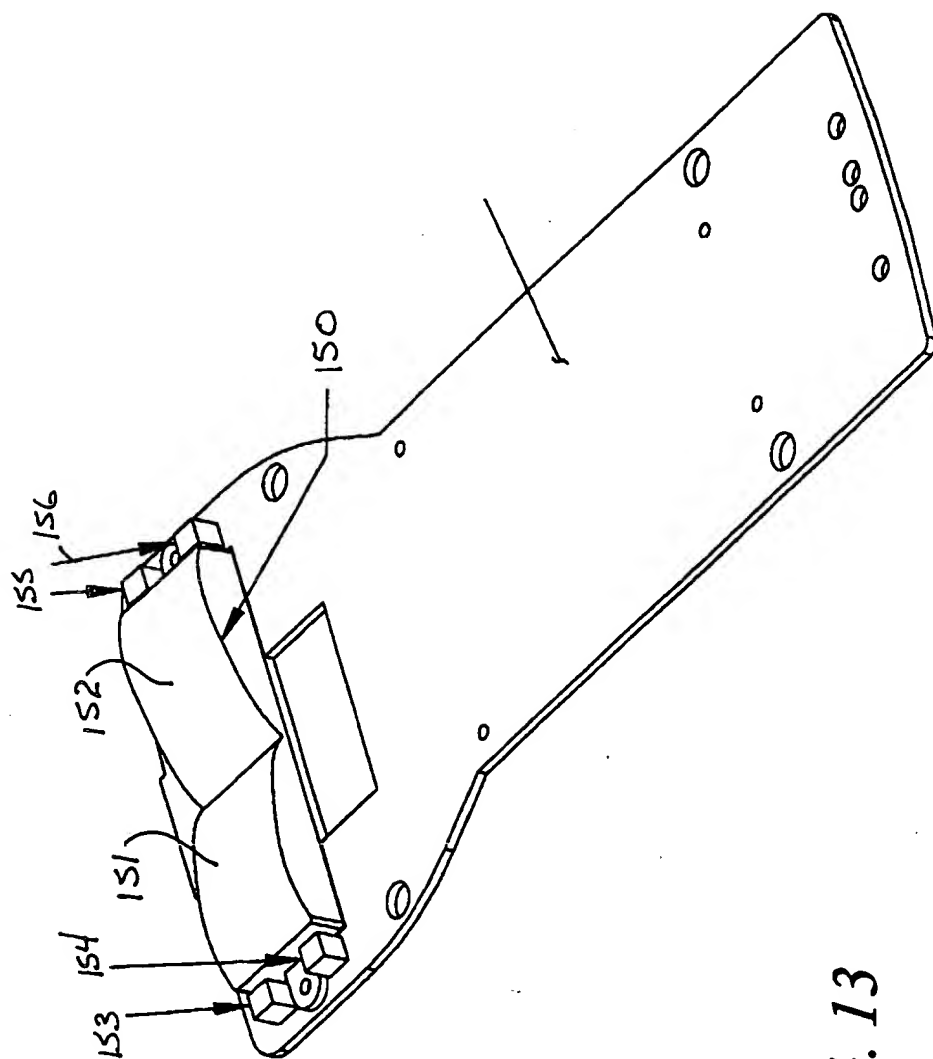


FIG. 13

FIG. 13 is a perspective view of the assembly shown in FIG. 12, with the top surface of the main plate (150) and the side surface of the angled component (151) shown.

FIG. 14 is a perspective view of the device 100 in a folded position. The device 100 includes a base 160 and a telescopic tube 165. The base 160 is supported by three legs 160a, 160b, and 160c. The telescopic tube 165 is mounted on the base 160 and is in a retracted position. The device 100 also includes a lens 164 and a camera 166. The camera 166 is mounted on the base 160 and is in a retracted position. The device 100 is shown in a perspective view.

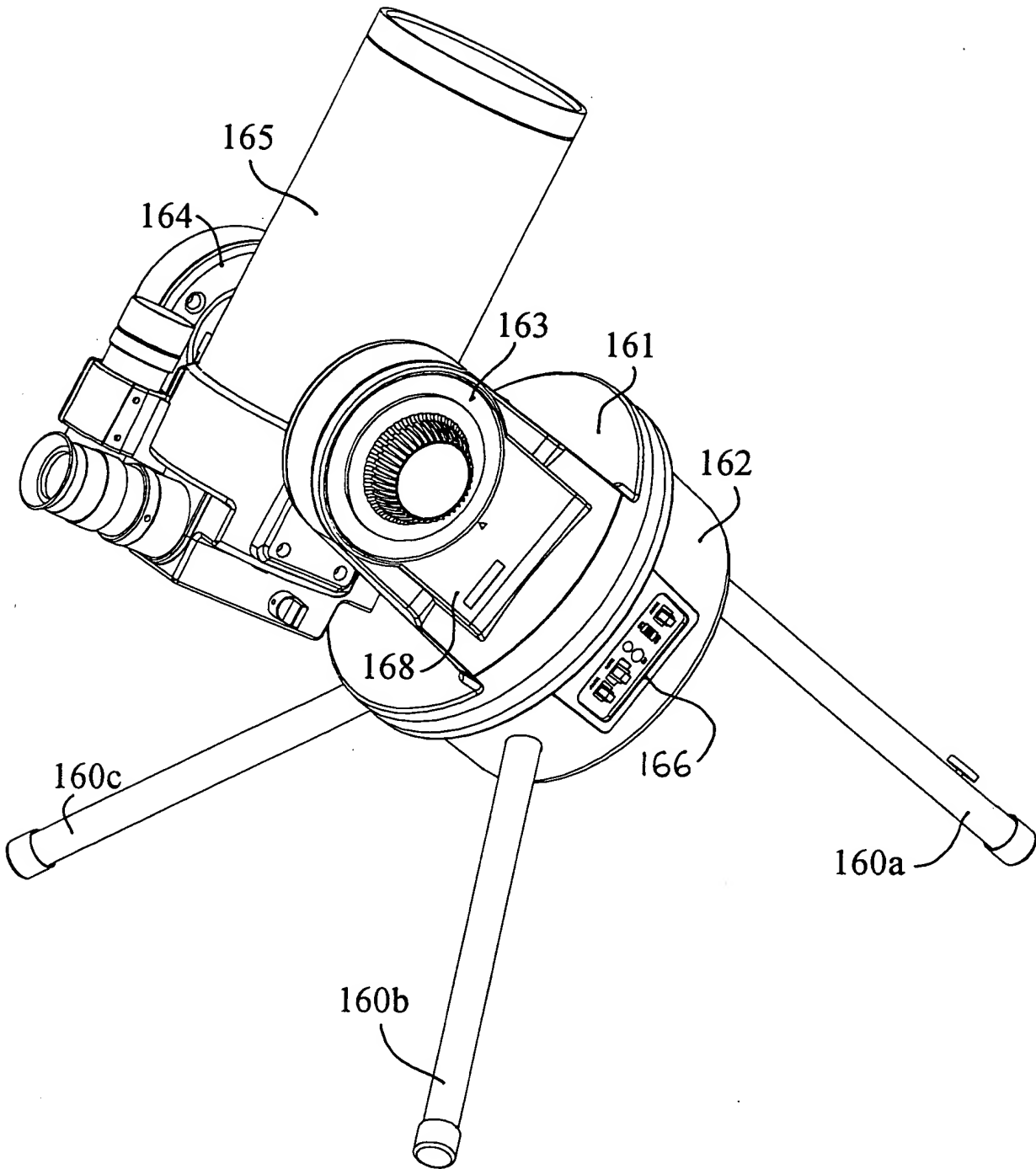


FIG. 14

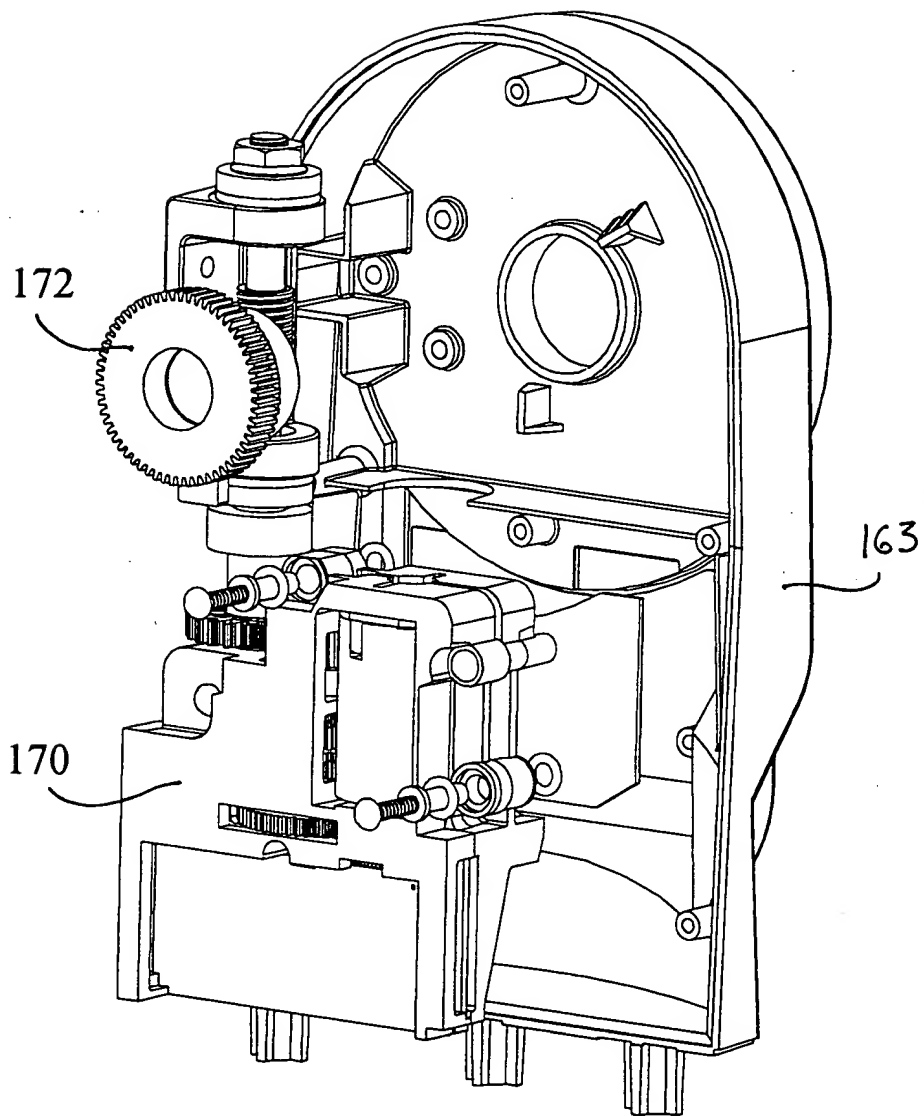


FIG. 15

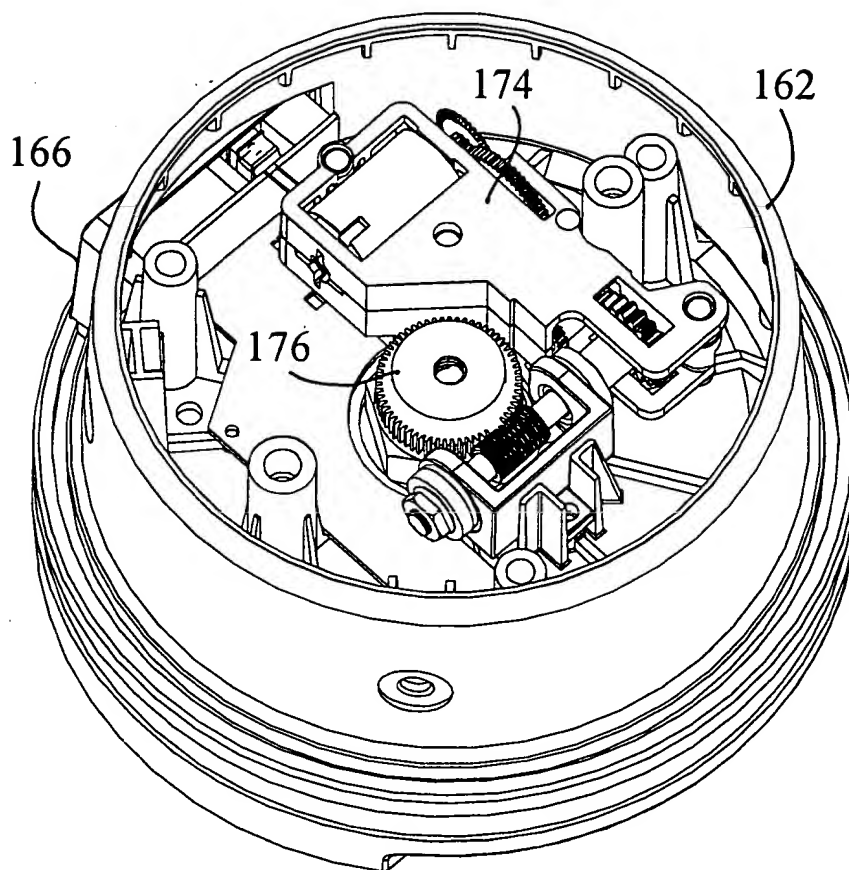


FIG. 16

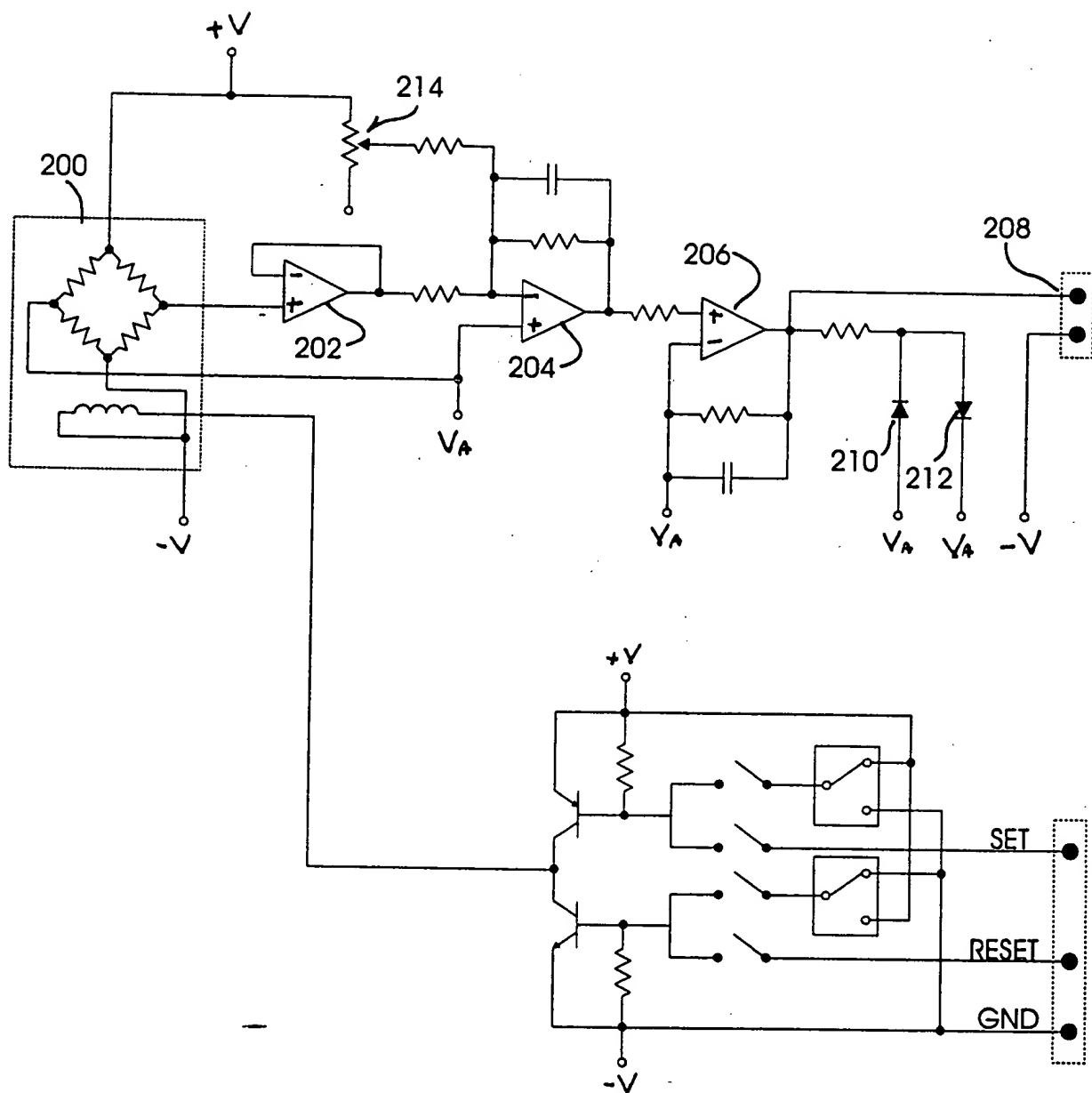


FIG. 17